

FIG. 1

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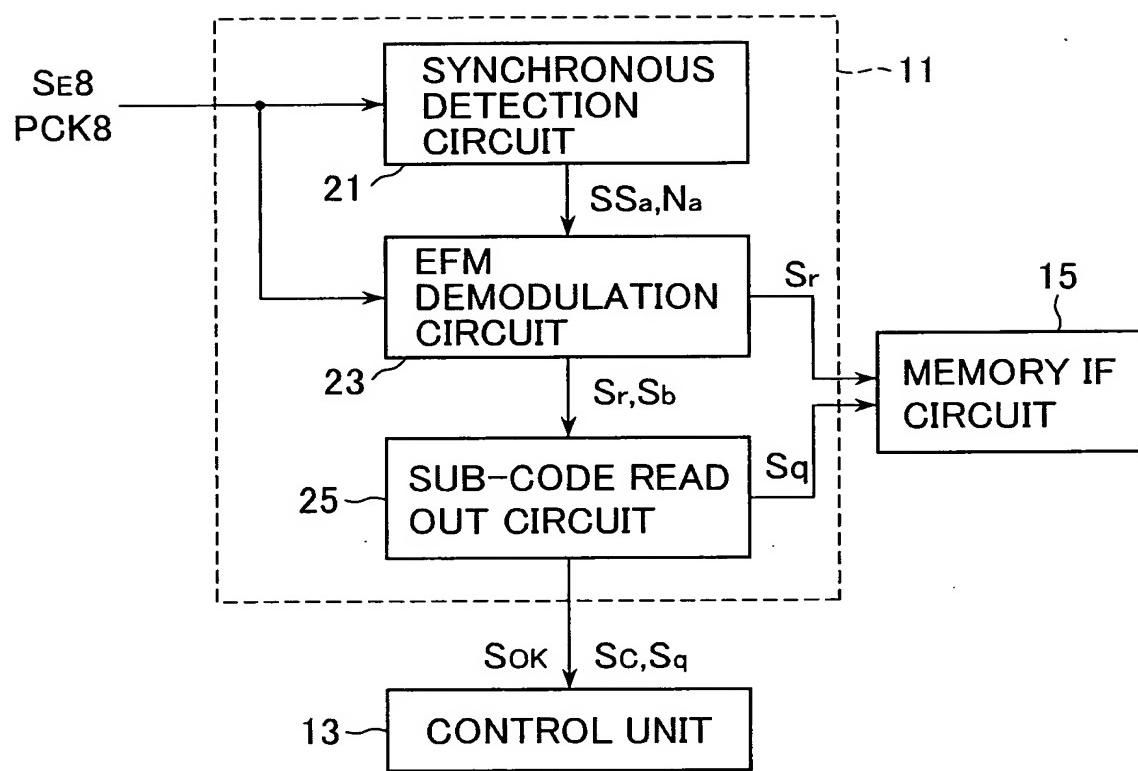


FIG. 2

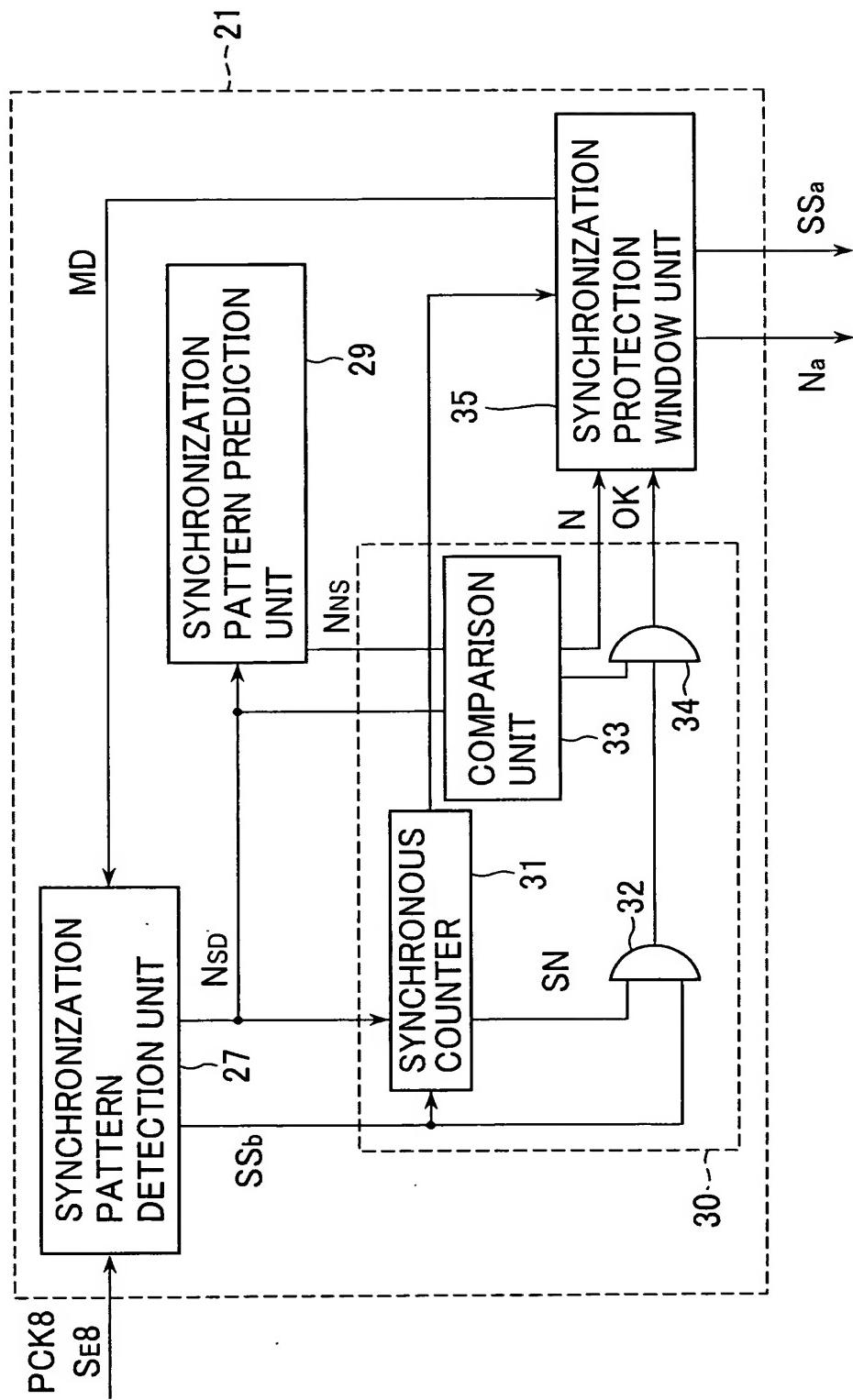


FIG. 3

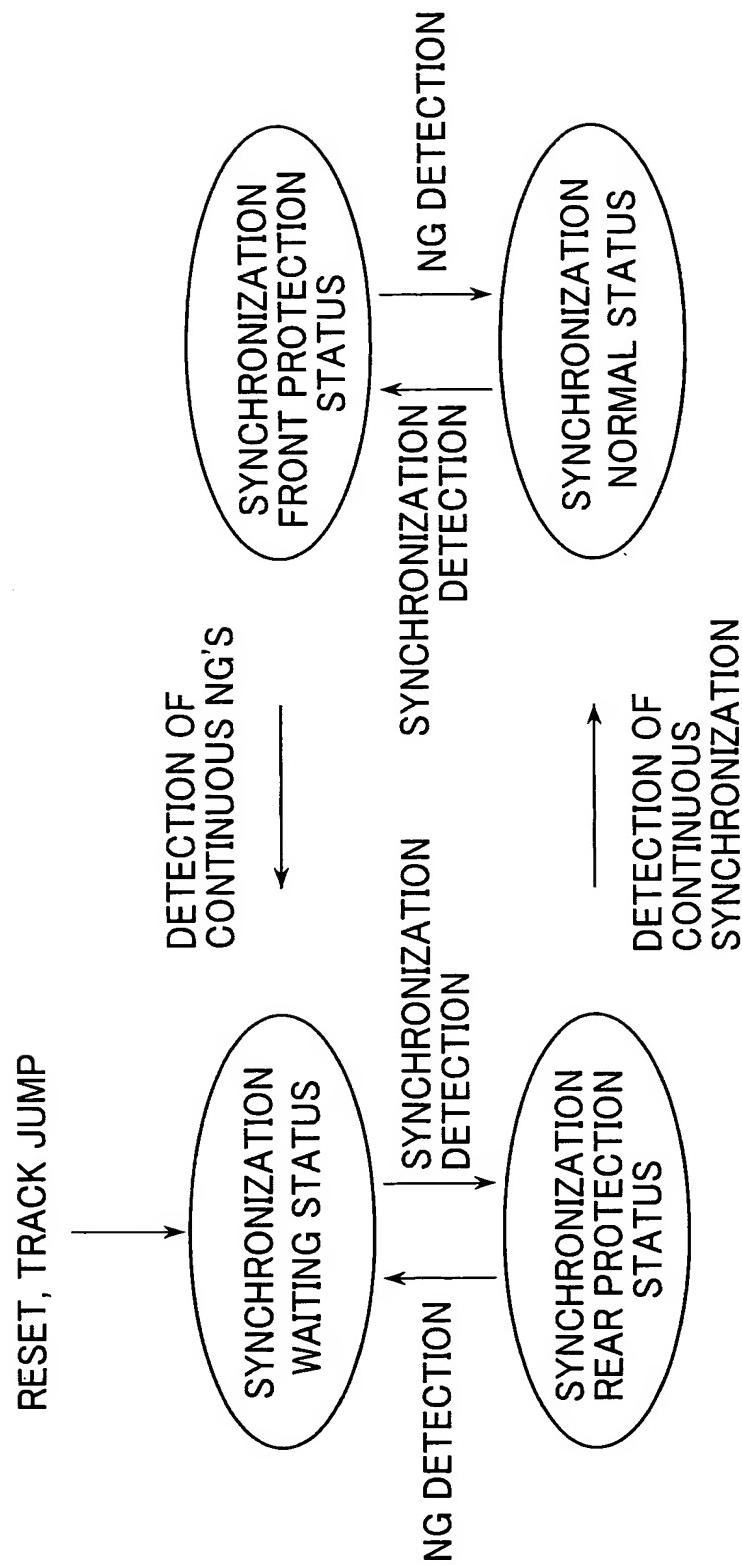


FIG. 4

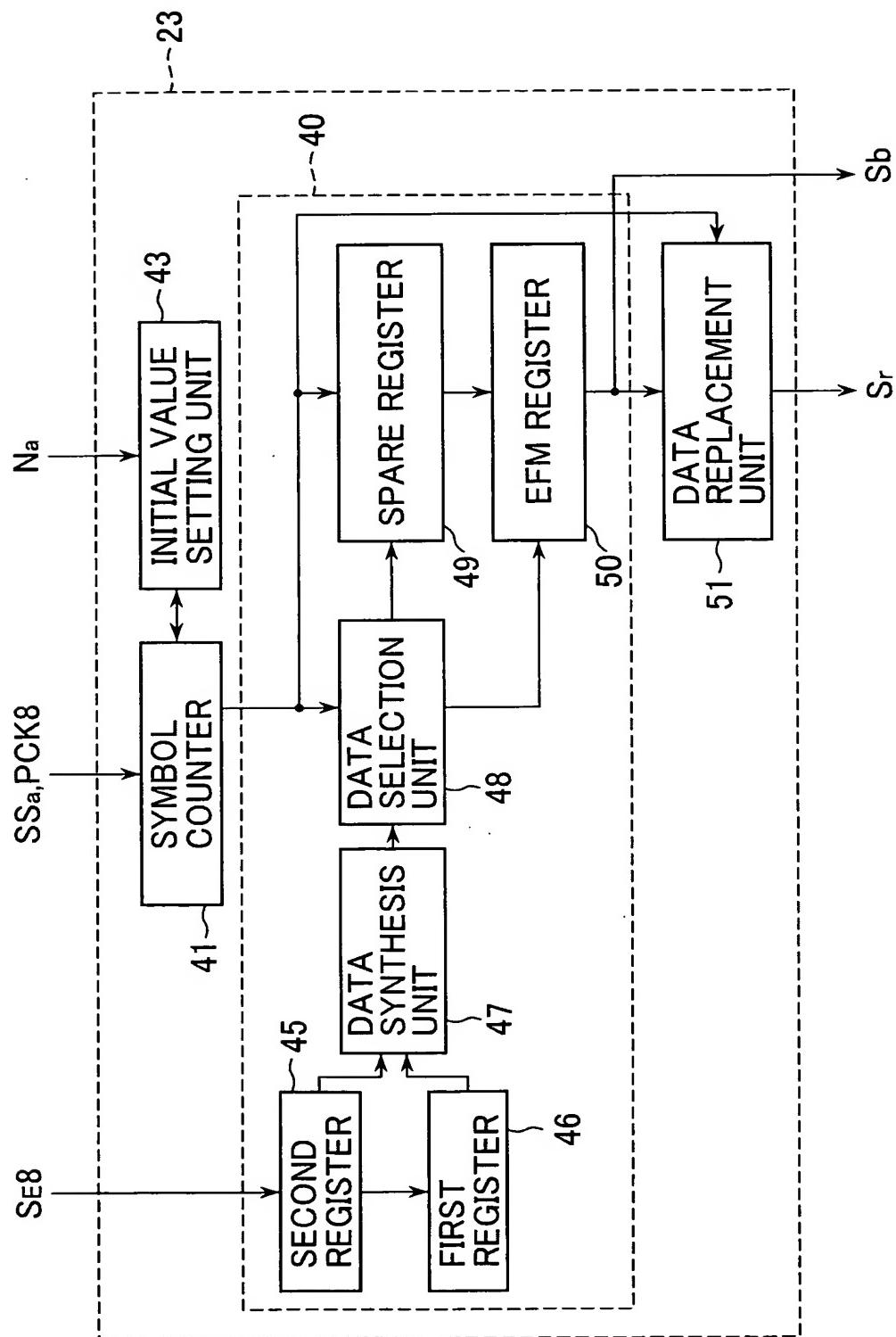


FIG. 5

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DSV	3	14	3	14	3
THIRTY-SECOND DATA					
DSV					
THIRTY-FIRST DATA					
DSV					
DSV					
THIRD DATA					
DSV					
SECOND DATA					
DSV					
FIRST DATA					
DSV					
SUB-CODE					
DSV	24	3	14	3	14
FRAME SYNCHRONIZATION SIGNAL					

FIG. 6

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COUNTER VALUE	EFM REGISTER[14:1]			SPARE REGISTER[14:1]	
0	R[1:0]	"		-	R[15:5]
1	-	R[15:5]	←	-	
2	R[2:0]	"		-	R[15:6]
3	-	R[15:6]	←	-	
4	R[3:0]	"		-	R[15:7]
5	-	R[15:7]	←	-	
6	R[4:0]	"		-	R[15:8]
7	-	R[15:8]	←	-	
8	R[5:0]	"		-	R[15:9]
9	-	R[15:9]	←	-	
10	R[6:0]	"		-	R[15:10]
11	-	R[15:10]	←	-	
12	R[7:0]	"		-	R[15:11]
13	-	R[15:11]	←	-	
14	R[8:0]	"		-	R[15:12]
15	-	R[15:12]	←	-	
16	R[9:0]	"		-	R[15:13]
17	-	R[15:13]	←	-	
18	R[10:0]	"		-	R[15:14]
19	-	R[15:14]	←	-	
20	R[11:0]	"		-	R[15]
21	-	R[15]	←	-	
22	R[12:0]	"		-	
23	-			-	
24	R[13:0]			-	
25	-			-	
26	R[14:1]			-	
27	-			-	
28	R[15:2]			-	
29	-			-	
30	-		-	R[15:3]	
31	-	R[15:3]	←	-	
32	R[0]	"	-	R[15:4]	
33	-	R[15:4]	←	-	

FIG. 7